

DS-C30S-DEC**Decoding Board**

The video wall controller is mainly used for screen splicing control system, and is the core control device of the system. As a new-generation FPGA-based pure hardware image processing device, it adopts the dual data switching technology and the structure of main control board and service boards. It supports processing and transmitting large data, processing multiple high-definition and ultra-high-definition signals in real time, and managing multiple screens.

- Supports outputting a maximum of 16 channels of 1080p@30 fps data.
- Supports decoding a maximum of 16 MP.
- Supports a maximum of 8 decoding boards.

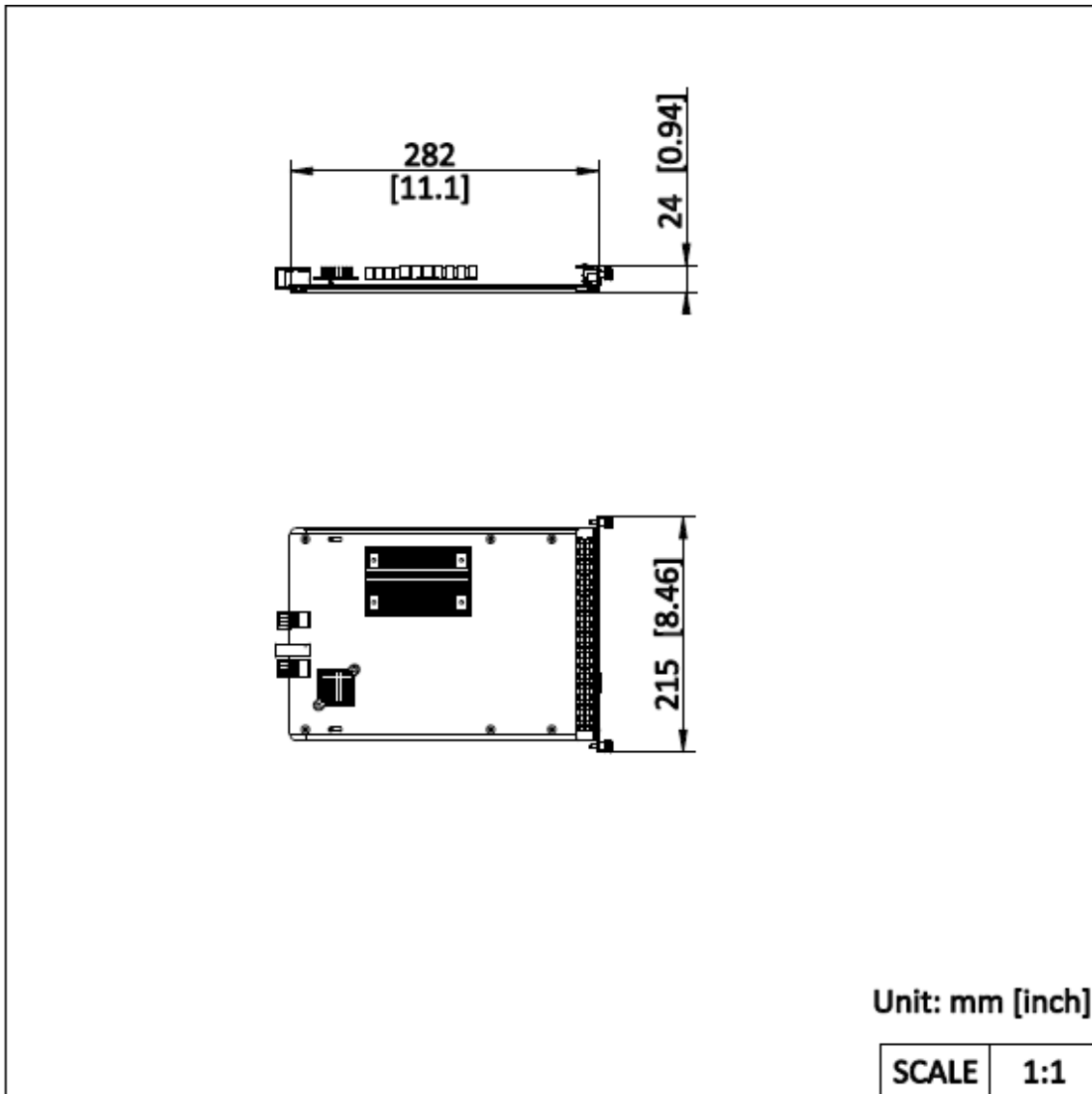
▪ Specification

Model		DS-C30S-DEC
Chassis	Occupied Slot	Input slot
Video Decoding	Video Decoding Format	H.264, H.265, Smart264, Smart265, MJPEG
	Video Decoding Channels	32
	Video Decoding Capability	Supports decoding a maximum of 16 channels of 1080p@30 fps, 32 channels of 720p@30 fps, 4 channels of 8 MP, or 1 channel of 16 MP. Smart H.265/Smart H.264/H.264/H.265: 16 channels of 1080p@30 fps MJPEG: 2 channels of 1080p@30 fps
	Video Decoding Resolution	≤ 16 MP
Audio Decoding	Audio Decoding Format	G711-A, G711-U, G722.1, G726-16/U, MPEG, AAC-LC

▪ Available Model

DS-C30S-DEC

▪ Dimension



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